

31040 U.S. PRO
09/784151
02/16/01

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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PATENT NUMBER

U.S. UTILITY Patent Application

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| O.I.P.E. | PATENT DATE |
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| SCANNED <u>12/24</u> | Q.A. <u>ST</u> |

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| APPLICATION NO. 09/784151 | CONT/PRIOR F | CLASS 380 713 | SUBCLASS 168 | ART UNIT 207 2132 2137 | EXAMINER Dov G. H. H. |
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| TITLE | APPLICANTS |
|-------------------------|-------------------------|
| 1. Chairman | 1. Chairman |
| 2. Vice Chairman | 2. Vice Chairman |
| 3. Secretary | 3. Secretary |
| 4. Treasurer | 4. Treasurer |
| 5. Member | 5. Member |
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| 100. Member | 100. Member |

Yusuke Kawasaki
Shigeru Hashimoto

IC, IC-mounted electronic device, debugging method and IC debugger

PTO-2040
12/99

| ISSUING CLASSIFICATION | | | | | | | | | | | | |
|------------------------------|--|----------|--|--------------------|-----------------------------------|--|--|--|--|--|--|--|
| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | | | |
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | | |
| INTERNATIONAL CLASSIFICATION | | | | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | _____ (Assistant Examiner) (Date) | | NOTICE OF ALLOWANCE MAILED | |
| | _____ _____ _____ _____ (Primary Examiner) (Date) | | ISSUE FEE | |
| Amount Due | | | Date Paid | |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | ISSUE BATCH NUMBER | |

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